

FIG. 9A

PROGRAM LOAD

(80010h, 12 BYTES, ADD ---, SUB ---)
 (AND ---, OR ---, XOR ---, LD.W ---)
 WRITE (80010h, ADD ---, SUB ---)
 + WRITE (80014h, ADD ---, OR ---)
 + WRITE (80018h, XOR ---, LD.W ---)

FIG. 9B

STEP EXECUTION
 WRITE TO STEP EXECUTION ENABLE
 BIT OF CONTROL REGISTER

+GO

MINI ← please add it

FIG. 9C

INTERNAL REGISTER READ
 READ OF MONITOR RAM ON MEMORY MAP

FIG. 9D

BREAKPOINT SETTING
 WRITE TO BREAK ENABLE BIT AND
 BREAK ADDRESS BIT OF CONTROL
 REGISTER

11/17 (con't)

FIG. 11 (con't)

